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Bib Data Sheet

CONFIRMATION NO. 7656

								
SERIAL NUMBE 10/830,019	FILING DATE 04/23/2004 RULE	CLASS 716	GROUP ART 2825	JP ART UNIT 2825		ATTORNEY DOCKET NO. 042264-0101		
APPLICANTS								
Yoshiki Kashiwagi, Kanagawa, JAPAN;								
Tetsuya Kat	toh, Kanagawa, JAPAN;					.		
** CONTINUING DATA **********************************								
** FOREIGN APPLICATIONS ************************************								
IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 06/30/2004								
Foreign Priority claimed	7,00	STATE OR	SHEETS	TOTA	٦L	INDEPENDENT		
35 USC 119 (a-d) condit met Verified and	Allowance To	fter 77 COUNTRY nitials JAPAN	DRAWING 8	CLAIN	VIS-	CLAIMS		
Acknowledged	Examiner's Signature	Illiais U/N / NN		8		3		
ADDRESS 22428 FOLEY AND LARDNER LLP SUITE 500 3000 K STREET NW WASHINGTON , DC 20007								
TITLE Method for designing semiconductor circuit device, utilizing boundary cells between first and second circuits driven by different power supply systems								
			□ All	Fees				
	,			16 Fees (Filing	j)		
i . i	FEES: Authority has been on the charge of th	credit DEPOSIT ACCC	OUNT 1.1	☐ 1.17 Fees (Processing Ext. of time)				
856			1.18			3 Fees (Issue)		
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